

## 单通道ESD 保护二极管

**UM5059 DFN2 1.0×0.6**

### 描述

UM5059 ESD保护二极管设计用于取代手机、笔记本电脑和PDA等便携式应用中的MLV。与MLV相比，该器件内置可传导高瞬态电流的大截面积结，具有板级保护的电气特性，例如快速响应时间、较低的工作电压、较低的钳位电压和无器件劣化。UM5059 ESD 保护二极管可保护敏感的半导体元件免受静电放电 (ESD) 和其他瞬态电压事件的损坏或破坏。UM5059采用 DFN2 1.0 × 0.6（与SOD923和SOD882兼容）封装，工作电压为5 V。在阵列不实用的应用中，设计人员可灵活地使用该器件保护单向线路。此外，在电路板空间有限的应用中，该器件可采用分散布置的布局方案。该器件可满足IEC 61000-4-2标准的静电抗扰度要求：±30kV 空气间隙放电和 ±30kV 接触放电。

### 应用

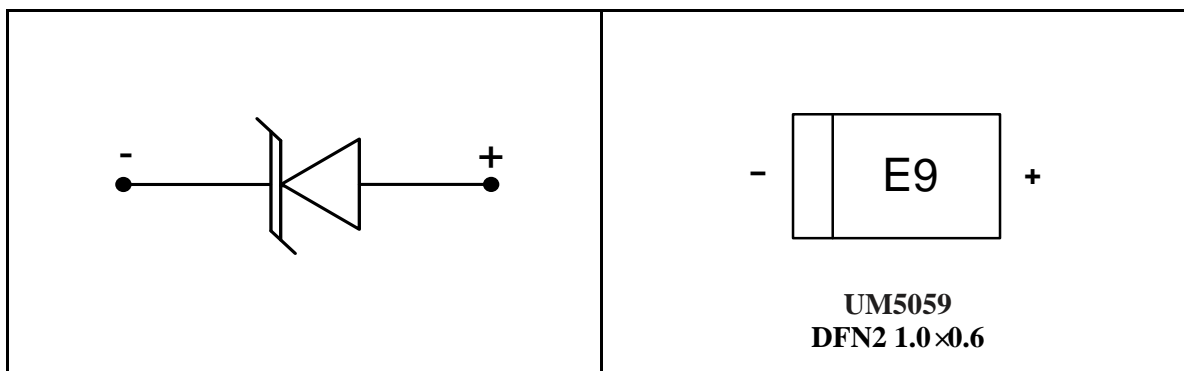
- 手机听筒和配件
- PDA
- 笔记本电脑、台式机和服务器
- 便携式设备
- 无线电话
- 数码相机
- 外围设备
- MP3 播放器

### 特性

- 数据线瞬态保护，符合 IEC 61000-4-2标准：±30kV（空气间隙放电），±30kV（接触放电）
- 用于便携式电子设备的小型封装
- ESD保护应用中MLV的合适替代品
- 保护一路I/O或电源线
- 低钳位电压
- 反向工作电压：5V
- 低漏电流
- 固态硅雪崩技术
- 小尺寸外形：1.0mm × 0.6mm

### 引脚配置

### 顶部视图



## Ordering Information

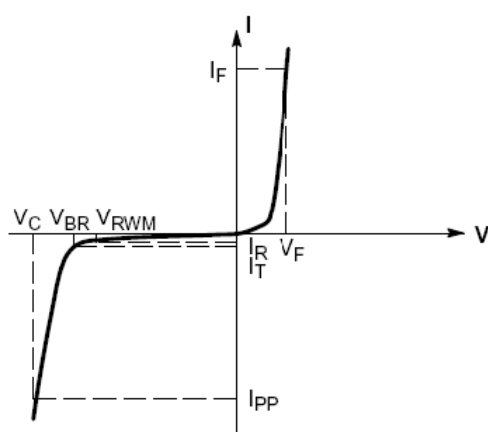
Part Number	Working Voltage	Packaging Type	Channel	Marking Code	Shipping Qty
UM5059	5.0V	DFN2 1.0×0.6	1	E9	5000pcs/7 Inch Tape & Reel

## Absolute Maximum Ratings

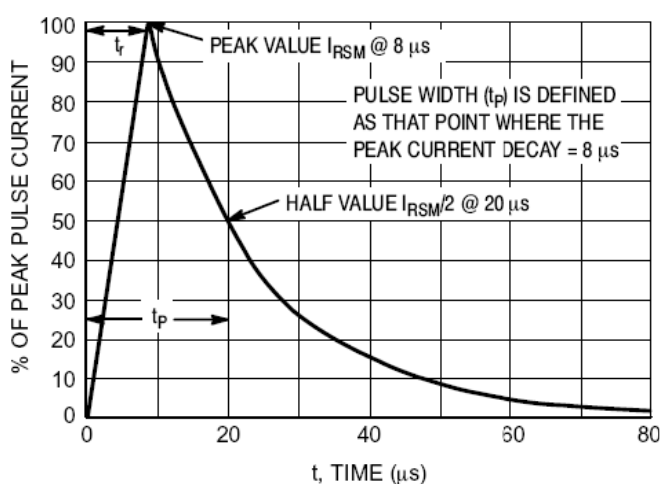
Rating	Symbol	Value	Unit
Peak Pulse Power ( $t_p=8/20\mu s$ )	$P_{PK}$	140	Watts
Maximum Peak Pulse Current ( $t_p=8/20\mu s$ )	$I_{PP}$	11	Amps
Lead Soldering Temperature	$T_L$	260 (10 sec.)	°C
Operating Temperature	$T_J$	-55 to +125	°C
Storage Temperature	$T_{STG}$	-55 to +150	°C

## Symbol Definition

Parameter	Symbol
Maximum Reverse Peak Pulse Current	$I_{PP}$
Clamping Voltage @ $I_{PP}$	$V_C$
Working Peak Reverse Voltage	$V_{RWM}$
Maximum Reverse Leakage Current @ $V_{RWM}$	$I_R$
Breakdown Voltage @ $I_T$	$V_{BR}$
Test Current	$I_T$
Forward Current	$I_F$
Forward Voltage @ $I_F$	$V_F$
Peak Power Dissipation	$P_{PK}$
Max. Capacitance @ $V_R=0V$ , $f=1MHz$	$C$



Uni-Directional TVS



## Electrical Characteristics

(T=25 °C, Device for 5.0V Reverse Stand-off Voltage)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse Stand-Off Voltage	$V_{RWM}$				5	V
Reverse Breakdown Voltage	$V_{BR}$	$I_T=1mA$	6	6.8	7.8	V
Reverse Leakage Current	$I_R$	$V_{RWM}=5V, T=25\text{ °C}$			0.1	$\mu A$
Clamping Voltage	$V_C$	$I_{PP}=5A, t_P=8/20\mu s$			9.1	V
		$I_{PP}=11A, t_P=8/20\mu s$			13	
Forward Voltage	$V_F$	$I_F=10mA$		0.8		V
Junction Capacitance	$C_J$	$V_R=0V, f=1MHz$		40	55	pF
Junction Capacitance	$C_J$	$V_R=2.5V, f=1MHz$		30	40	pF

## Applications Information

### Device Connection Options

UM5059 ESD protection diode is designed to protect one data, I/O, or power supply line. The device is unidirectional and may be used on lines where the signal polarity is above ground. The cathode dot should be placed towards the line that is to be protected.

### Circuit Board Layout Recommendations for Suppression of ESD

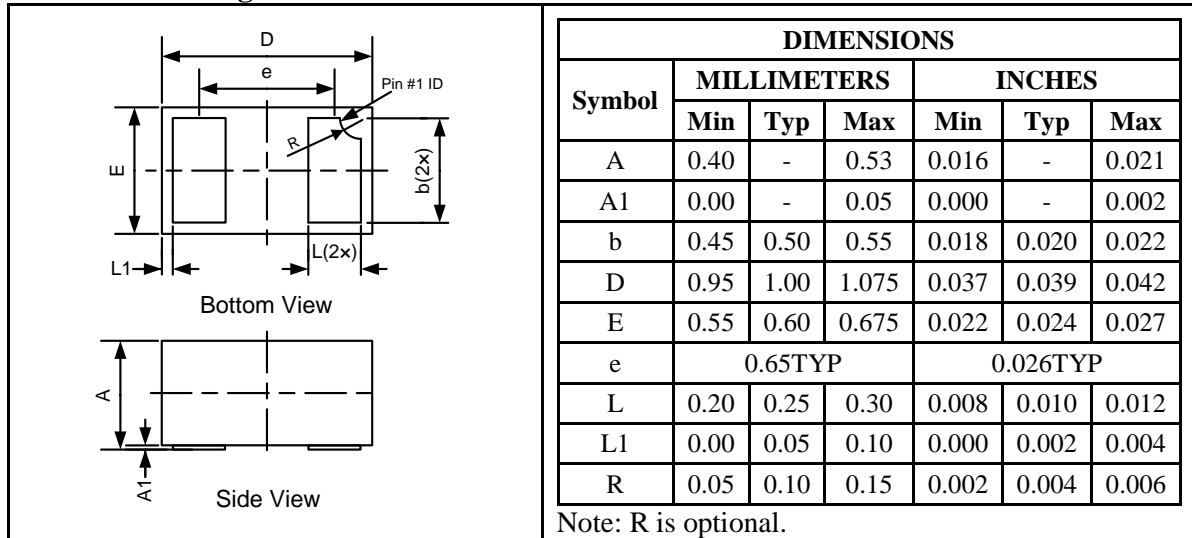
Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

1. Place the TVS near the input terminals or connectors to restrict transient coupling.
2. Minimize the path length between the TVS and the protected line.
3. Minimize all conductive loops including power and ground loops.
4. The ESD transient return path to ground should be kept as short as possible.
5. Never run critical signals near board edges.
6. Use ground planes whenever possible. For multilayer printed-circuit boards, use ground vias.
7. Keep parallel signal paths to a minimum.
8. Avoid running protection conductors in parallel with unprotected conductor.
9. Minimize all printed-circuit board conductive loops including power and ground loops.
10. Avoid using shared transient return paths to a common ground point.

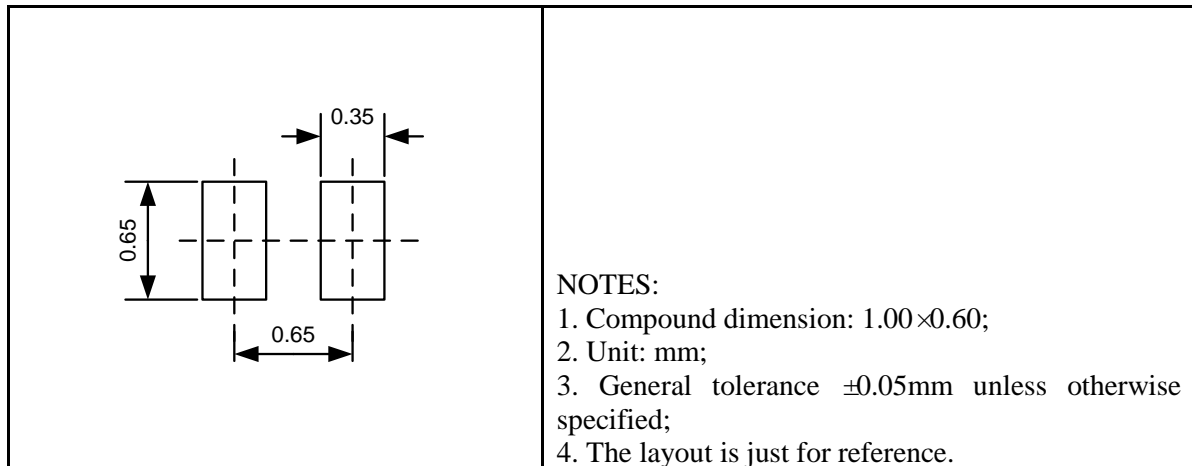
## Package Information

### UM5059 DFN2 1.0×0.6

#### Outline Drawing



#### Land Pattern



#### Tape and Reel Orientation



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