

1A、2.25MHz、同步降压DC-DC转换器

UM3510S SOT23-6

UM3510DA DFN6 2.0×2.0

描述

UM3510 是高效的脉宽调制(PWM)同步降压 DC-DC 转换器,输入电压范围为 2.5V 至 6.0V,可为单块锂离子电池提供高达 1000mA 的输出电流。UM3510 在 2.25MHz 固定开关频率下运行,进入省电模式后可在轻负载条件下保持高效率。

对于低噪声应用,UM3510 可通过将 MODE 引脚的电平拉高来强制芯片进入固定频率脉宽调制(PWM)模式。

当 EN 引脚拉低时,UM3510 进入关断模式,工作电流降至 1μA 以下。该器件还具备 2%精度的低内部基准电压、过温保护和过流保护等其他特性。

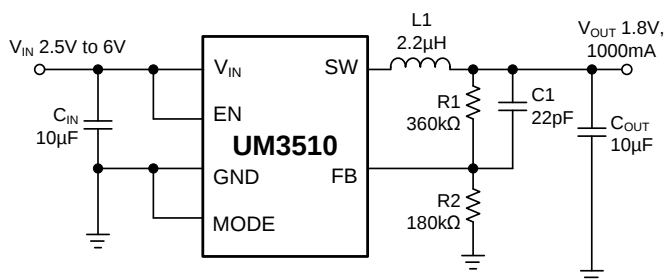
UM3510 采用 SOT23-6 和 6 引脚 DFN 2mm×2mm 封装。

应用

特性

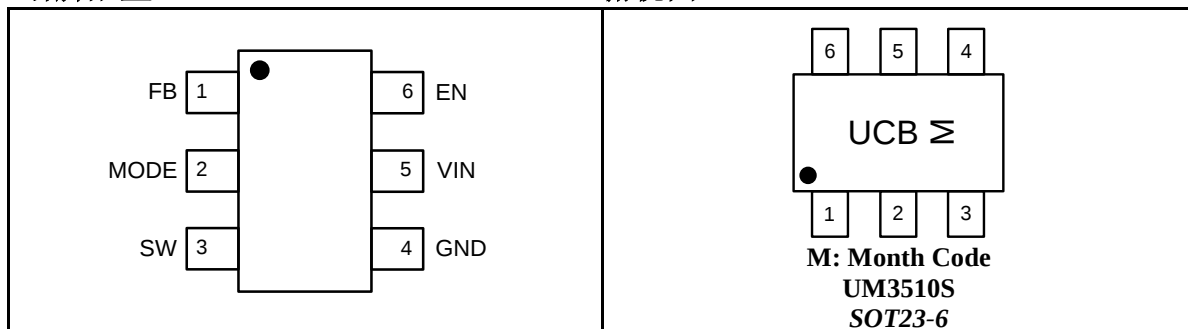
- 蜂窝电话和智能手机
- 微处理器和 DSP 核心电源
- 无线和 DSL 解调器
- PDA、GPS
- WLAN
- 便携式仪器
- 效率高达 95%
- 恒定开关频率: 2.25MHz
- 输出电流: 1000mA
- 集成主开关和同步整流器,无需肖特基二极管
- 输入电压范围: 2.5V 至 6.0V
- 低静态电流: 56μA
- 过温保护
- 关断电流: <1μA

典型应用电路



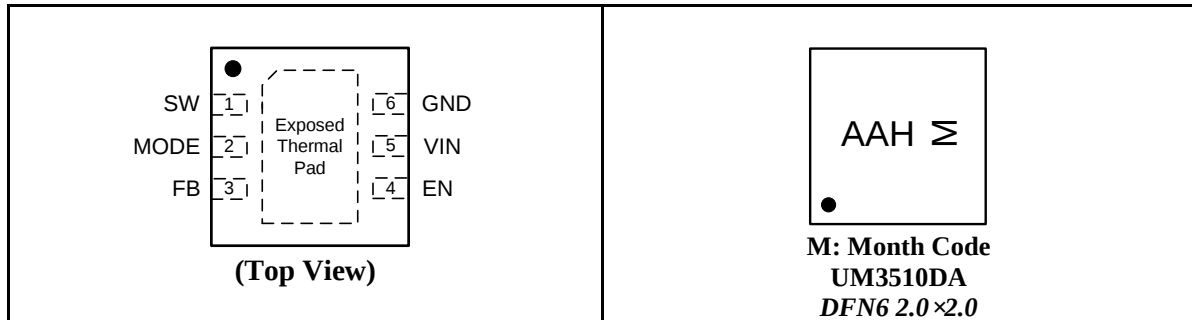
引脚配置

俯视图



Pin Configurations

Top View



Ordering Information

Part Number	Packaging Type	Marking Code	Shipping Qty
UM3510S	SOT23-6	UCB	3000pcs/7Inch Tape & Reel
UM3510DA	DFN6 2.0×2.0	AAH	

Pin Description

Pin Number		Symbol	Function
SOT23-6	DFN6 2.0×2.0		
3	1	SW	Power switch output. It is the switch node connection to the inductor. This pin connects to the drains of the internal P-CH and N-CH MOSFET switches.
2	2	MODE	MODE pin=high forces the device to operate in fixed-frequency PWM mode. Mode pin=low enables the Power Save Mode with automatic transition from PFM mode to fixed-frequency PWM mode.
1	3	FB	Feedback input pin. Connect FB to the center point of the external resistor divider.
6	4	EN	Regulator enable control input. Pulling this pin to high enables the device. Pulling this pin to low forces the device into shutdown mode. This pin must be terminated.
5	5	VIN	Input voltage.
4	6	GND	Ground.

Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
V_{IN}	Input Voltage	-0.3 to +6.0	V
V_{EN}, V_{FB}	EN, FB Voltages	-0.3 to $V_{IN}+0.3$	V
V_{SW}	SW Voltage	-0.3 to $V_{IN}+0.3$	V
I_{SW}	Peak SW Sink and Source Current	2.0	A
T_O	Operating Temperature	-40 to +85	°C
T_{STG}	Storage Temperature Range	-65 to +150	°C

Note 1: Stresses greater than those listed under Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Thermal Capabilities

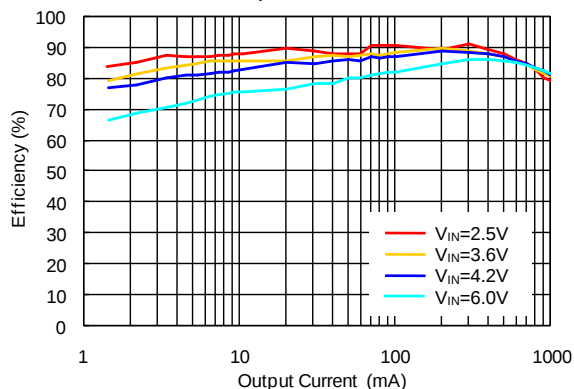
Symbol	Description		Value	Unit
θ_{JA}	Thermal Resistance	SOT23-6	190	°C/W
		DFN6 2×2	165	°C/W
P_D	Power Dissipation	SOT23-6	0.526	W
		DFN6 2×2	0.606	W
$\Delta P/^{\circ}C$	Derating Factor above $T_A=25^{\circ}C$	SOT23-6	-5.26	mW/°C
		DFN6 2×2	-6.06	mW/°C

Electrical Characteristics
 $(V_{IN}=V_{EN}=3.6V, T_A=+25\text{ }^{\circ}\text{C}, \text{ unless otherwise noted})$

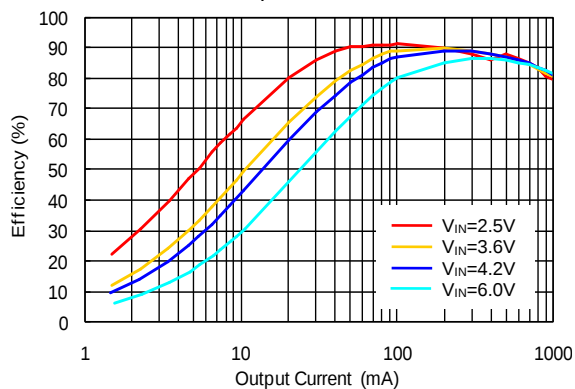
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
SUPPLY						
V_{IN}	Input Voltage Range		2.5		6.0	V
UVLO	Under Voltage Lockout Threshold	Falling Edge	1.8		2.3	V
UVLO_HYS	Under Voltage Lockout Hysteresis			200		mV
I_Q	Input DC Supply Current (MODE=GND)	$I_{LOAD}=0\text{mA}$		56	80	μA
	Input DC Supply Current (MODE=VIN)	$I_{LOAD}=0\text{mA}$		5.6		mA
I_{SD}	Input Supply Current (Shutdown Mode)	$V_{FB}=0V, V_{IN}=6.0V$		0.1	1.0	μA
OUTPUT						
V_{FB}	Regulated Feedback Voltage	$T_A=+25\text{ }^{\circ}\text{C}$	0.588	0.600	0.612	V
I_{FB}	FB Input Bias Current	$V_{FB}=0.65V$			30	nA
	Output Voltage Line Regulation	$2.5V \leq V_{IN} \leq 6.0V$, $I_{OUT}=10\text{mA}$, MODE=VIN/GND		0.10	0.20	%/V
	Output Voltage Load Regulation	$0.1A \leq I_{OUT} \leq 1A$		0.5		%/A
$I_{O(MAX)}$	Maximum Output Current		1000			mA
tss	Soft Start Time			400		μs
I_{SWL}	SW Leakage	$V_{EN}=0V, V_{IN}=6V$, $V_{SW}=0V \text{ or } 6V$		± 0.01	± 1	μA
POWER SWITCH						
$R_{DS(ON)}$	$R_{DS(ON)}$ of P-CH MOSFET	$V_{IN}=3.6V$, $I_{SW}=100\text{mA}$		0.25	0.30	Ω
	$R_{DS(ON)}$ of N-CH MOSFET	$V_{IN}=3.6$, $I_{SW}=-100\text{mA}$		0.18	0.23	Ω
I_P	Peak Inductor Current	$V_{IN}=2.5V \text{ to } 6.0V$	1.6	1.8	2.0	A
	Thermal Shutdown Temperature			150		$^{\circ}\text{C}$
	Thermal Shutdown Temperature Hysteresis			20		$^{\circ}\text{C}$
OSILLATOR						
f	Oscillator Frequency		1.95	2.25	2.55	MHz
ENABLE, MODE						
V_{IH}	High-Level Threshold		1.2			V
V_{IL}	Low-Level Threshold				0.4	V
I_I	Input Leakage Current			0.1	1	μA

Typical Operating Characteristics

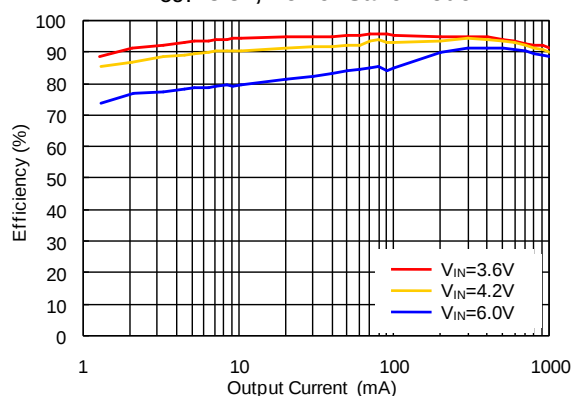
Efficiency vs. Load Current
V_{OUT}=1.8V, Power Save Mode



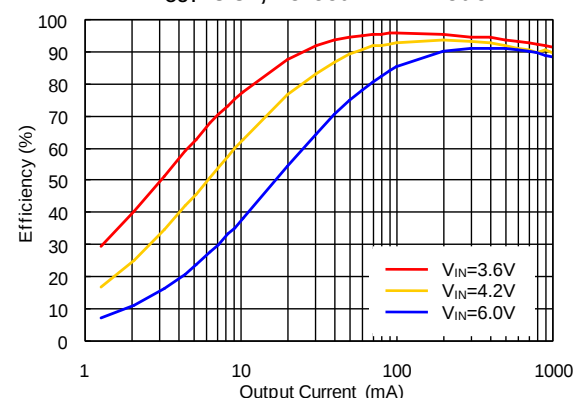
Efficiency vs. Load Current
V_{OUT}=1.8V, Forced PWM Mode



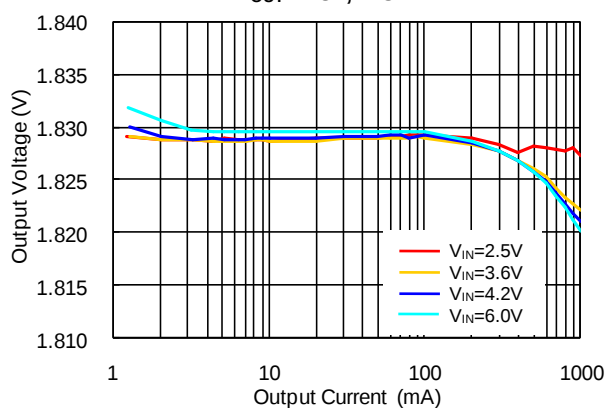
Efficiency vs. Load Current
V_{OUT}=3.3V, Power Save Mode



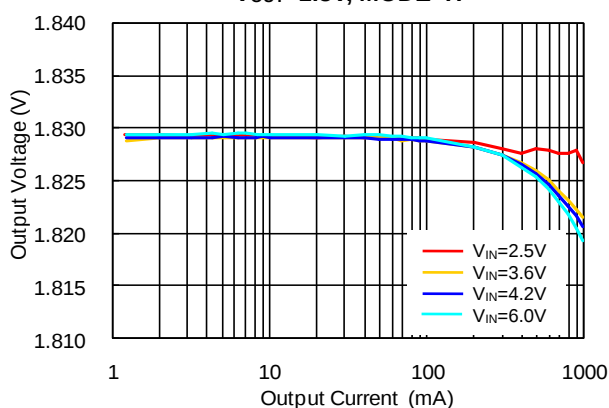
Efficiency vs. Load Current
V_{OUT}=3.3V, Forced PWM Mode



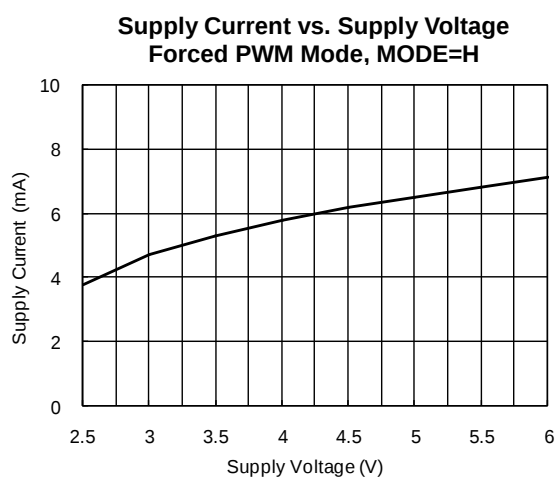
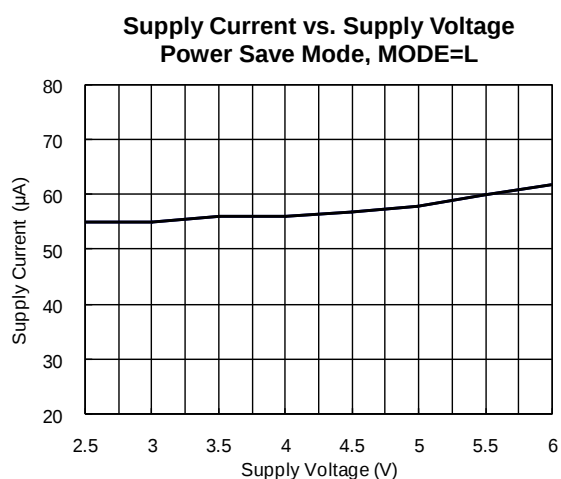
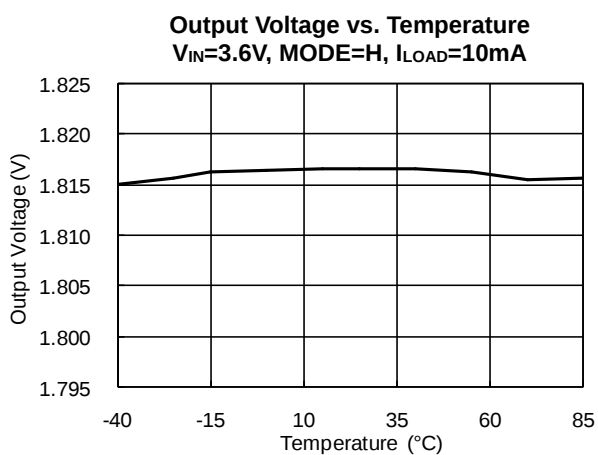
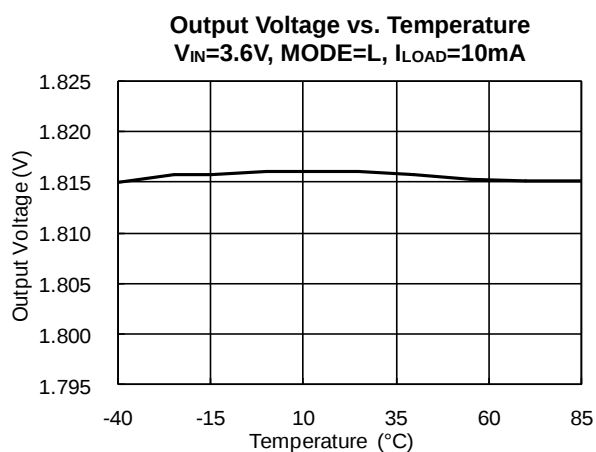
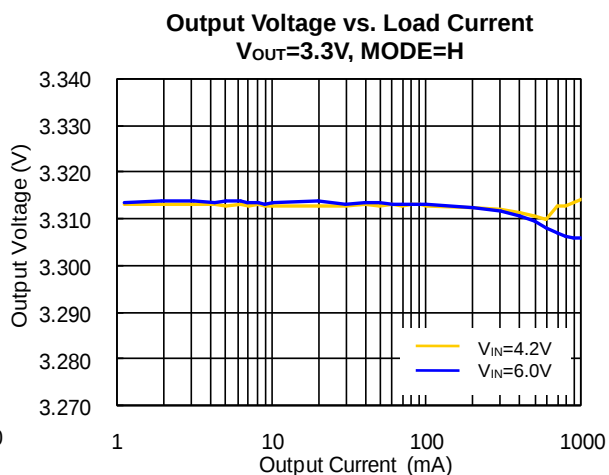
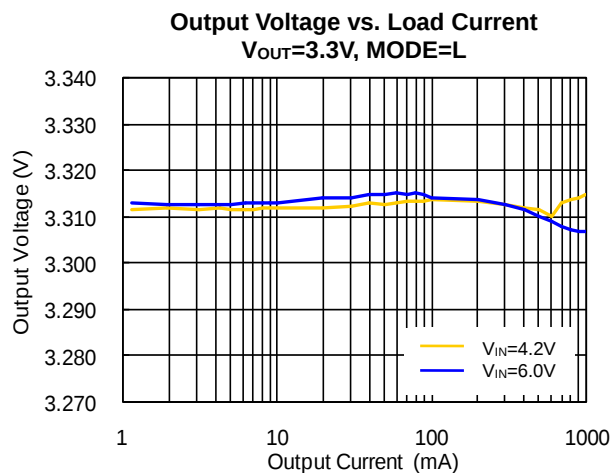
Output Voltage vs. Load Current
V_{OUT}=1.8V, MODE=L



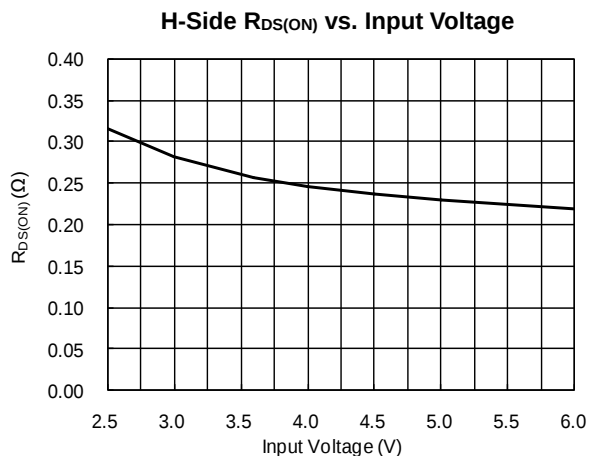
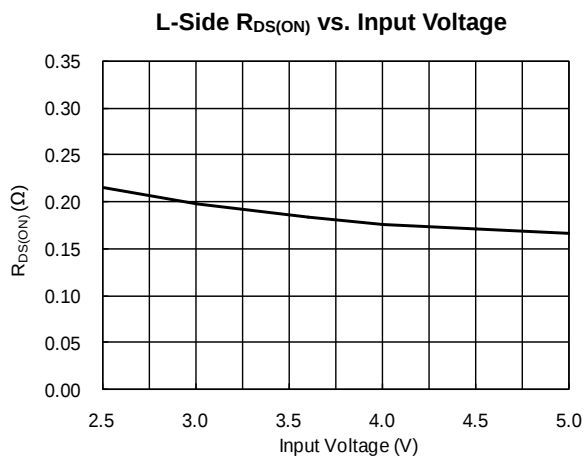
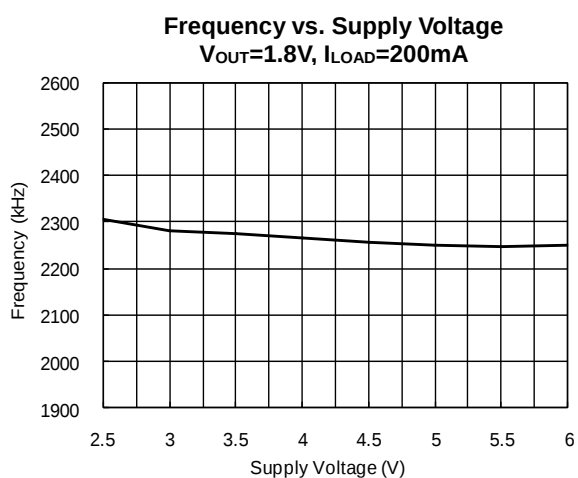
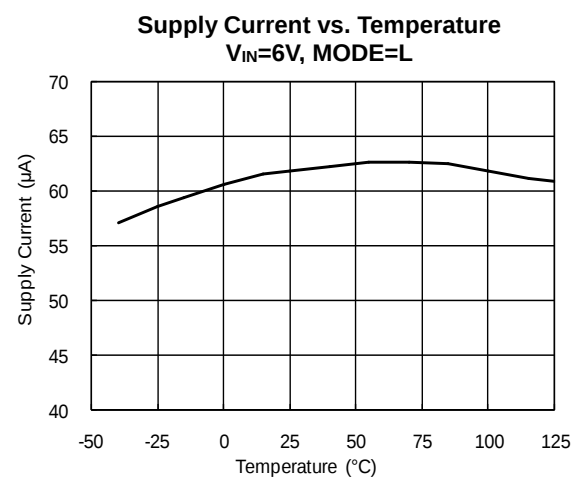
Output Voltage vs. Load Current
V_{OUT}=1.8V, MODE=H



Typical Operating Characteristics (Continued)

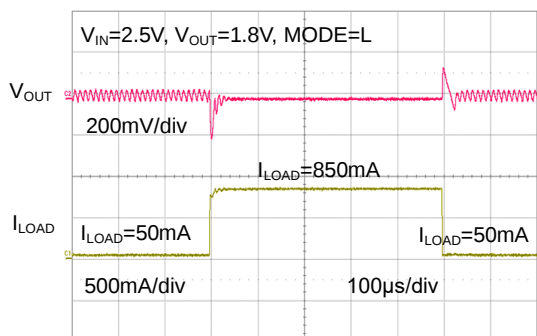


Typical Operating Characteristics (Continued)

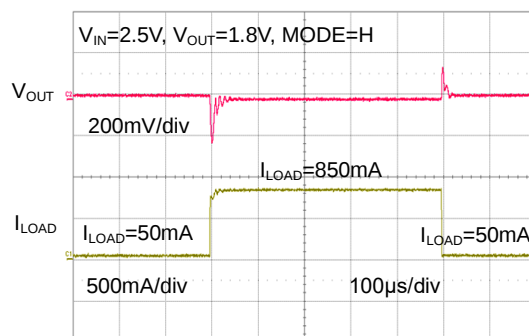


Typical Operating Characteristics (Continued)

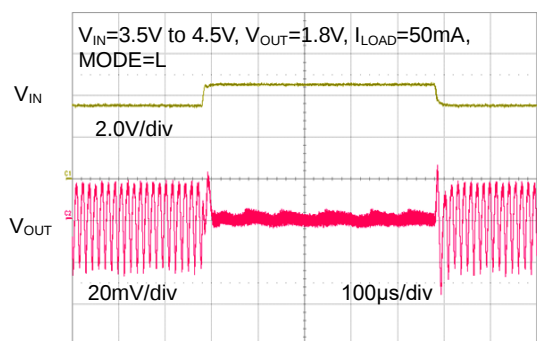
Load Transient Response



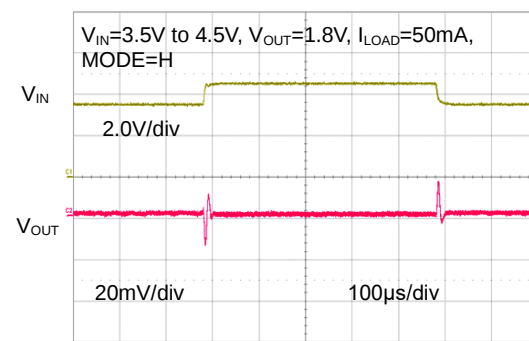
Load Transient Response



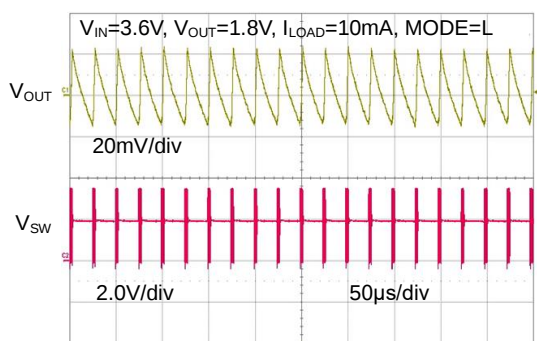
Line Transient Response



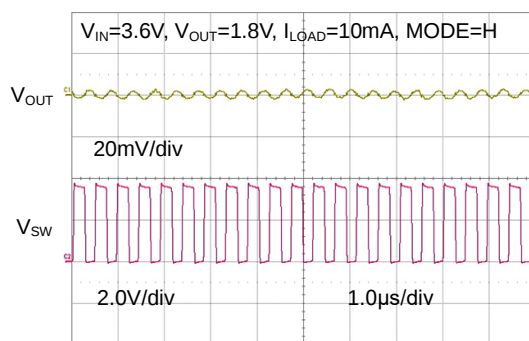
Line Transient Response



PFM Mode Operation



PWM Mode Operation



Application Information

Output Voltage Setting

The output voltage can be calculated according to the formula below with an internal reference voltage V_{REF} typical 0.6V:

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R1}{R2}\right)$$

To minimize the current through the feedback divider network, the recommended value of R2 is about 180k Ω . The sum of R1 and R2 should not exceed about 1M Ω to keep the network robust against noise. An external feed forward capacitor C1 is required for optimum load transient response. The value of C1 should be in the range between 10pF and 33pF.

Route the FB line away from noise sources, such as the inductor or the SW line.

Inductor Selection

A 1 μ H to 10 μ H inductor with DC current rating at least 25% higher than the maximum load current is recommended for most applications. For best efficiency, the inductor DC resistance shall be <200m Ω .

For most designs, the inductance value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is the inductor ripple current. Choose inductor ripple current approximately 30% of the maximum load current, 1000mA.

The maximum inductor peak current is:

$$I_{L(MAX)} = I_{LOAD} + \frac{\Delta I_L}{2}$$

The following table is a list of recommended inductors.

List of Recommended Inductors

Dimension [mm ³]	Inductor Type	Supplier
4.5×4×3.2	744773022	Würth Elektronik
3×3×1.5	LQH3NPN2R2NM0	MURATA
3×3×1.5	LPS3015	Coilcraft

Input Capacitor Selection

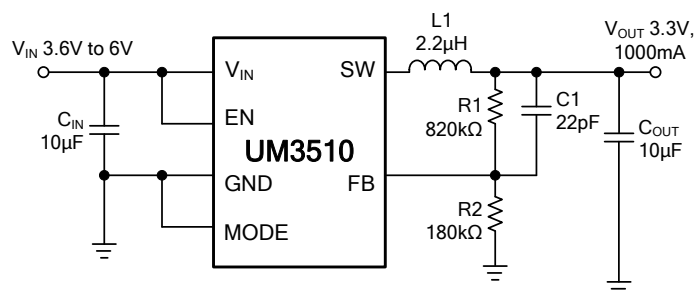
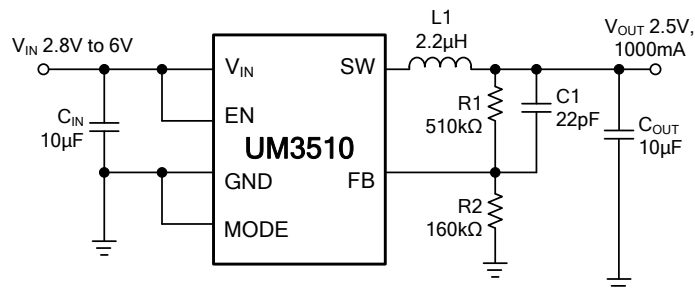
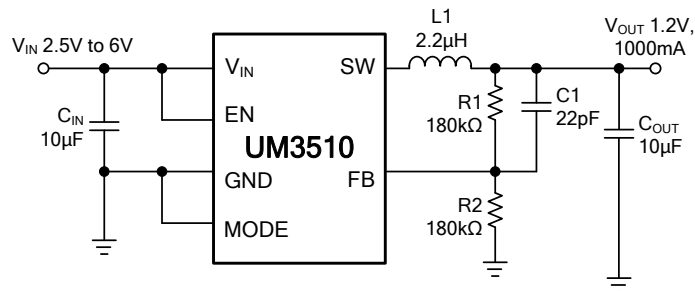
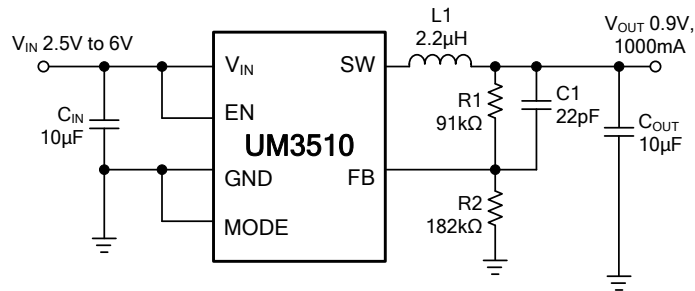
The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency shall be less than the input source impedance to prevent high frequency switching current passing to the input. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. For most applications, a 10 μ F capacitor is sufficient. The input capacitor can be increased without any limit for better input voltage filtering.

Output Capacitor Selection

The output capacitor keeps output voltage ripple small and ensures regulation loop stable. The output capacitor impedance shall be low at the switching frequency. Ceramic capacitor with X5R or X7R dielectrics are recommended. The output ripple ΔV_{OUT} is approximately:

$$\Delta V_{OUT} \leq \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times f_{OSC} \times L} \times \left(ESR + \frac{1}{8 \times f_{OSC} \times C_{OUT}}\right)$$

Typical Application Circuit



Layout Guidance

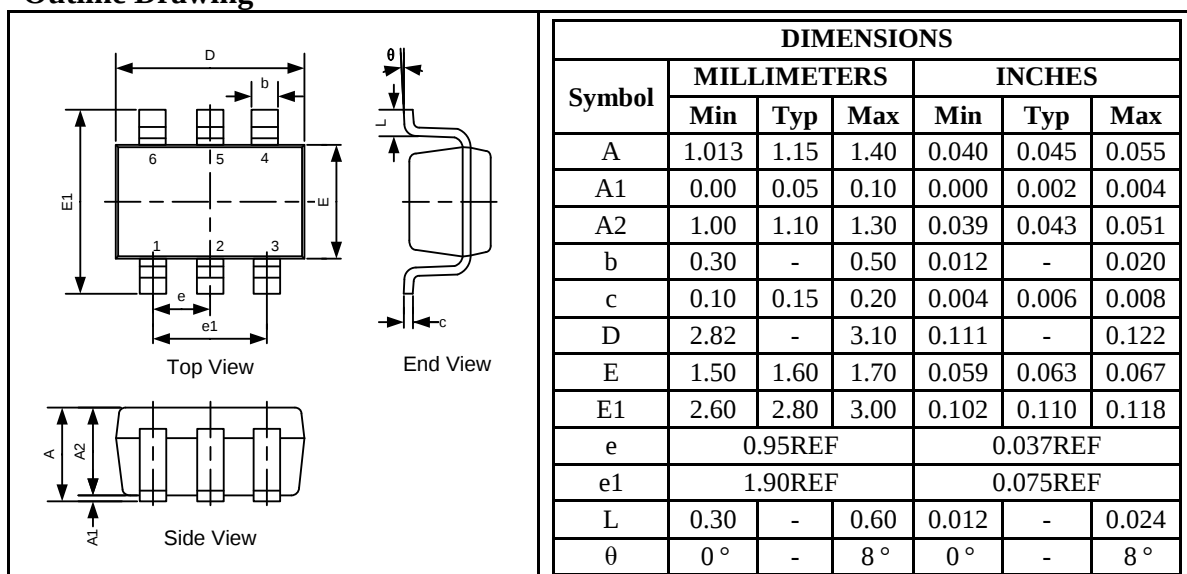
When laying out the PC board, the following suggestions should be taken to ensure proper operation of the UM3510.

1. The power traces, including the GND trace, the SW trace and the VIN trace should be kept short, direct and wide to allow large current flow.
2. Connect the input capacitor C_{IN} to the VIN pin as closely as possible to get good power filter effect.
3. Keep the switching node, SW, away from the sensitive FB node.
4. Do not trace signal line under inductor.

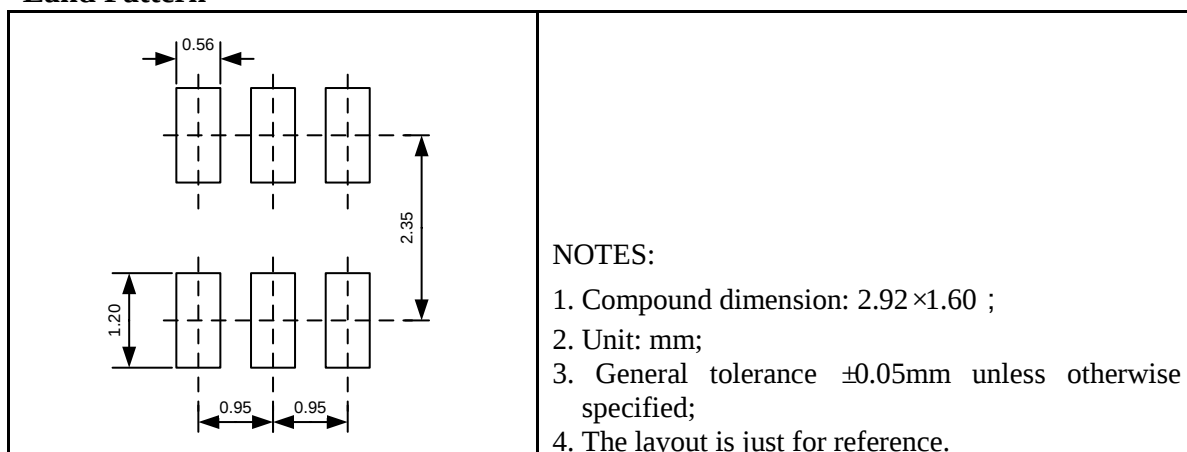
Package Information

UM3510S: SOT23-6

Outline Drawing



Land Pattern

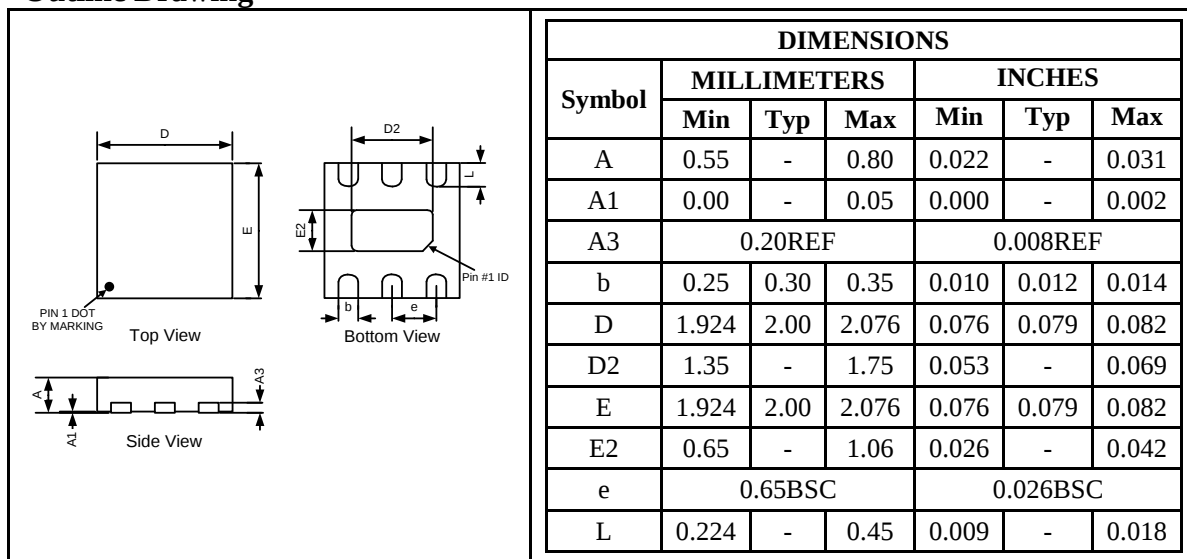


Tape and Reel Orientation

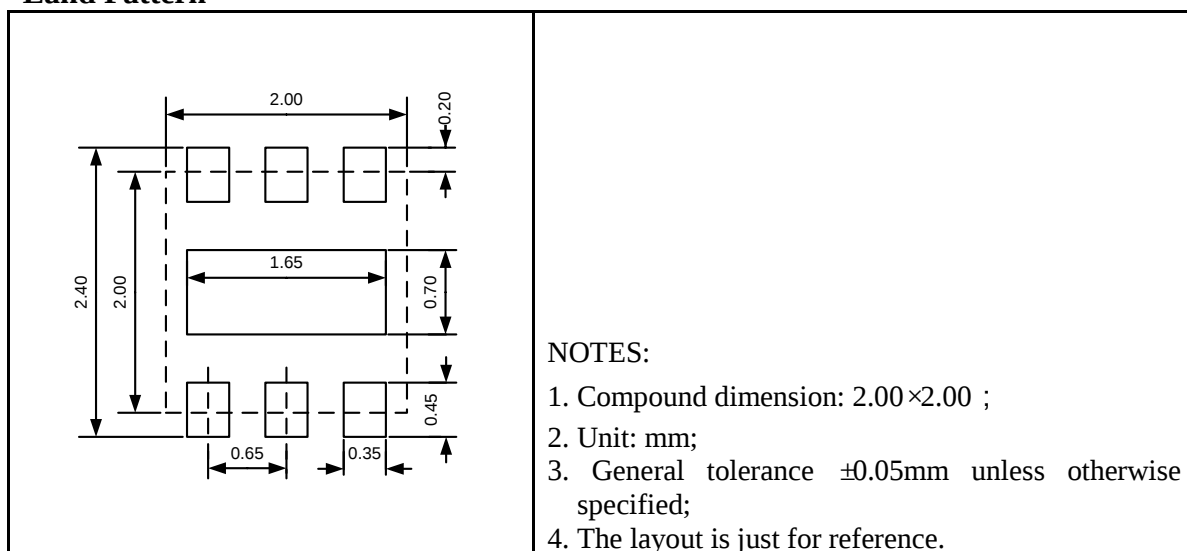


UM3510DA DFN6 2.0×2.0

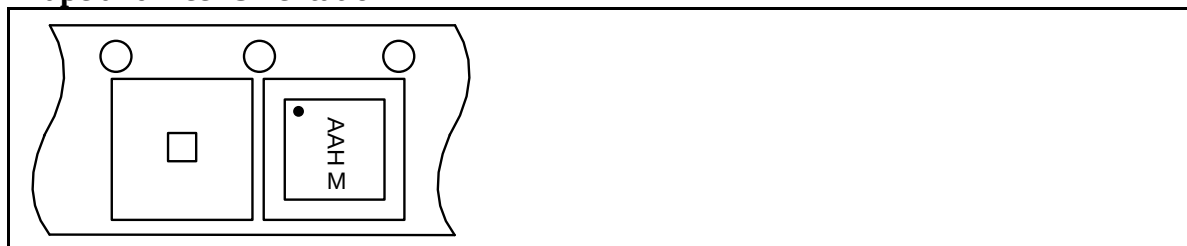
Outline Drawing



Land Pattern



Tape and Reel Orientation



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