

具备3V至5.5V供电、2发/2收、250kbps、关断模式 RS-232收发器

UM3222EEUE TSSOP20

1 描述

UM3222E是3.3V供电的RS-232收发器，适用于便携式或手持式应用。UM3222E具有两个驱动器和两个接收器。该器件具有低功耗、高数据速率能力和增强型ESD保护功能。所有发射器输出和接收器输入的ESD额定值在人体放电模式下为 $\pm 15\text{kV}$ ，在IEC61000-4-2空气间隙放电下为 $\pm 15\text{kV}$ ，在IEC61000-4-2接触放电下超过 $\pm 8\text{kV}$ 。逻辑I/O引脚的ESD额定值在人体放电模式下为 $\pm 2\text{kV}$ 。

UM3222E 具有低功耗关断模式，在该模式下，器件的驱动器输出和电荷泵处于禁用状态。关断状态下，电源电流降至 $1\mu\text{A}$ 以下，从而降低了便携式电池供电系统或其他低功耗系统的功耗。UM3222E 的接收器在关断模式下保持激活状态，允许监视外部信号。

UM3222E 具备小占板面积和扁平封装的特点，并使用 $0.1\mu\text{F}$ 电容，可节省电路板空间。在最大负载条件下，数据传输率大于 250kbps。

2 应用

- 工业自动化设备
- 电池供电设备
- 手持设备
- POS 终端

3 特性

- 通过+3.0V至+5.5V电源供电，符合真正的EIA/TIA-232-F标准
- 符合 EIA/TIA-562 $\pm 3.7\text{V}$ 的电平，电源电压低至 2.7V
- 增强ESD规格：
 - $\pm 15\text{kV}$ 人体放电模型
 - $\pm 15\text{kV}$ IEC61000-4-2 空气间隙放电
 - $\pm 8\text{kV}$ IEC61000-4-2 接触放电
- $1\mu\text{A}$ 低功耗关断模式
- 250kbps 最低传输速率
- 保证 $30\text{V}/\mu\text{s}$ 最大压摆率
- 闩锁性能(Latch-Up)超过200mA

4 订购信息

芯片型号	工作温度范围	封装类型	发货数量
UM3222EEUE	$-40\text{ }^{\circ}\text{C}$ to $+85\text{ }^{\circ}\text{C}$	TSSOP20	3000pcs/13 Inch Tape & Reel

5 Pin Configuration and Function

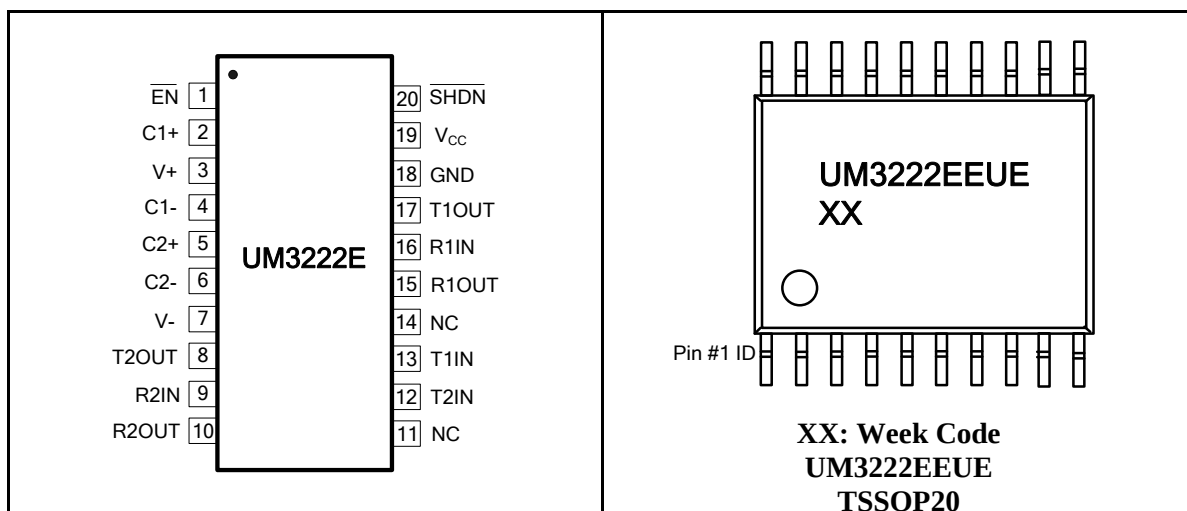


Table 5-1. Pin Functions

Pin No.	Pin Name	Function
1	$\overline{\text{EN}}$	Receiver Enable. Active low.
2	C1+	Positive Terminals of Voltage-Doubler Charge Pump Capacitor
3	V+	Positive Voltage Generated by the Charge Pump
4	C1-	Negative Terminals of Voltage-Doubler Charge Pump Capacitor
5	C2+	Positive Terminals of Inverting Charge Pump Capacitor
6	C2-	Negative Terminals of Inverting Charge Pump Capacitor
7	V-	Negative Voltage Generated by the Charge Pump
8, 17	T_OUT	RS-232 Driver Outputs
9, 16	R_IN	RS-232 Receiver Inputs
10, 15	R_OUT	RS-232 Receiver Outputs
11, 14	NC	Not connected
12, 13	T_IN	RS-232 Driver Inputs
18	GND	Ground
19	V _{CC}	+3.0V to +5.5V Supply Voltage Input
20	SHDN	Shut off Pump Power and Transmitters. Active low.

6 Specifications

6.1 Absolute Maximum Ratings (Note 1)

Symbol	Parameter		Value	Unit
V _{CC}	Supply Voltage on V _{CC}		-0.3 to +6	V
V ₊	Voltage on V ₊		(V _{CC} -0.3) to +7.5	V
V ₋	Voltage on V ₋		-7.5 to +0.3	V
T _{IN}	Voltage on T _{IN}		-0.3 to (V _{CC} +0.3)	V
R _{IN}	Voltage on R _{IN}		±30	V
T _{OUT}	Voltage on T _{OUT}		(V ₋ -0.3) to (V ₊ +0.3)	V
R _{OUT}	Voltage on R _{OUT}		-0.3 to (V _{CC} +0.3)	V
	Short-Circuit Duration, T _{OUT}		Continuous	
P _D	Continuous Power Dissipation at T _A =70 °C	TSSOP20	879	mW
T _A	Operating Temperature Range		-40 to +85	°C
T _{STG}	Storage Temperature Range		-65 to +165	°C
T _L	Lead Temperature for Soldering 10 Seconds		+260	°C

Note 1: Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

6.2 Electrical Characteristics

$V_{CC}=+3.0V$ to $+5.5V$, $C1-C4=0.1\mu F$, $T_A=T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A=25\text{ }^{\circ}C$.

Parameter	Symbol	Conditions		Min	Typ	Max	Unit
DC CHARACTERISTICS (V _{CC} =+3.3V or +5V, T _A =25 °C)							
V _{CC} Supply Current	I _{CC}	SHDN=V _{CC} , No Load				1.5	mA
Shutdown Supply Current	I _{SHDN}	SHDN=GND				1	μA
LOGIC INPUTS							
Input Leakage Current		T_IN, SHDN, EN				±1	μA
Input Threshold Low	V _{IL}	T_IN, SHDN, EN	V _{CC} =3.3V			0.8	V
			V _{CC} =5.0V			0.8	
Input Threshold High	V _{IH}	T_IN, SHDN, EN	V _{CC} =3.3V	1.6			V
			V _{CC} =5.0V	2.2			
Transmitter Input Hysteresis		V _{CC} =3.3V			0.2		V
		V _{CC} =5.0V			0.15		
RECEIVER OUTPUTS							
Output Voltage Low	V _{OL}	V _{CC} =3.3V, I _{OUT} =1.6mA				0.8	V
		V _{CC} =5.0V, I _{OUT} =1.6mA				0.8	
Output Voltage High	V _{OH}	V _{CC} =3.3V, I _{OUT} =-1.0mA		2.8			V
		V _{CC} =5.0V, I _{OUT} =-1.0mA		4.4			
RECEIVER INPUTS							
Input Voltage Range				-30		30	V
Input Threshold Low		T _A =+25 °C	V _{CC} =3.3V	0.8	1.15		V
			V _{CC} =5.0V	0.8	1.55		
Input Threshold High		T _A =+25 °C	V _{CC} =3.3V		1.35	2	V
			V _{CC} =5.0V		1.75	2	
Input Hysteresis					0.2		V
Input Resistance		T _A =+25 °C		3	5	7	kΩ
TRANSMITTER OUTPUTS							
Output Voltage Swing		All Drivers Loaded with 3kΩ to Ground		±5.0	±6.0		V
Output Short-Circuit Current		Short to V _{CC} , GND or Other TXD Pin	V _{CC} =3.3V		±30	±60	mA
			V _{CC} =5.0V		±40	±60	mA

Electrical Characteristics (Continued)

($V_{CC}=+3.0V$ to $+5.5V$, $C1-C4=0.1\mu F$, $T_A=T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A=25^\circ C$)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
TIMING CHARACTERISTICS						
Maximum Data Rate		$R_L=3k\Omega$ to $7k\Omega$, $C_L=50pF$ to $1000pF$, One Transmitter Switching		250		kbps
Receiver Propagation Delay	t_{PLH} , t_{PHL}	$C_L=150pF$, see Figure 8-1	All Parts, Normal Operation		0.15	μs
Receiver Skew	$ t_{PHL}-t_{PLH} $				0.1	μs
Transmitter Propagation Delay	t_{PLH} , t_{PHL}	$R_L=3k\Omega$, $C_L=2500pF$, All Transmitters Loaded, see Figure 8-1		0.9		μs
Transmitter Skew	$ t_{PHL}-t_{PLH} $				0.1	μs
Transition-Region Slew Rate		$T_A=+25^\circ C$, $V_{CC}=3.3V$, $R_L=3k\Omega$ to $7k\Omega$, $C_L=50pF$ to $1000pF$, Measured from $-3V$ to $+3V$ or $+3V$ to $-3V$, see Figure 8-1	3	15	30	$V/\mu s$
ESD AND LATCH UP PERFORMANCE						
R_IN, T_OUT ESD-Protection Voltage		Human Body Model		± 15		kV
		IEC61000-4-2, Contact Discharge		± 8		
		IEC61000-4-2, Air-Gap Discharge		± 15		
Logic Pin ESD-Protection Voltage		Human Body Model		± 2		kV
Latch Up Performance		JEDEC Standard No.78D		± 200		mA

7 Detailed Description

7.1 Dual Charge-Pump Voltage Converter

The UM3222E's internal power supply consists of a regulated dual charge pump and provides output the maxim voltages of +7V (doubling charge pump) and -7V (inverting charge pump) over the +3.0V to +5.5V V_{CC} range. The charge pump operates in discontinuous mode; if the output voltages are less than 7V, the charge pump is enabled, and if the output voltages exceed 7V, the charge pump is disabled. The charge pumps require only four small, external 0.1 μ F capacitors for the voltage doubler and inverter functions (see Figure 9-2).

7.2 RS-232 Transmitters

The transmitters are inverting level translators that translate TTL/CMOS inputs to EIA/TIA-232 output levels. All transmitters guarantee a 250kbps data rate for full load conditions (3k Ω and 1000pF). Transmitters can be paralleled to drive multiple receivers. When T_IN is not driven, UM3222E will keep logic high.

7.3 RS-232 Receivers

The receivers convert RS-232 signals to CMOS output levels and accept inputs up to ± 30 V while presenting the required 3k Ω to 7k Ω input impedance. The receivers have inverting three-state outputs. Drive EN high to place the receivers into a high impedance state. Receivers can be either active or inactive in shutdown state depending on EN status.

7.4 Shutdown Mode

Supply current falls to less than 1 μ A in shutdown mode ($\overline{\text{SHDN}} = \text{GND}$). When shutdown, the device's charge pumps are shut off, V+ is pulled down to V_{CC} , V- is pulled to ground, and the transmitter outputs are disabled (high impedance). The time required to recover from shutdown is typically 100 μ s. Connect $\overline{\text{SHDN}}$ to V_{CC} if shutdown mode is not used. $\overline{\text{SHDN}}$ has no effect on R_OUT.

7.5 ESD Protection

All pins on UM3222E include ESD protection structures, but the family incorporates advanced structures which allow the RS-232 pins (transmitter outputs and receiver inputs) to survive ESD events up to ± 15 kV. The RS-232 pins are particularly vulnerable to ESD damage because they are typically connected to an exposed port on the exterior of the finished product. The ESD structures withstand high ESD in all states: normal operation, shutdown and powered down. After an ESD event, circuits keep working without latch up. ESD protection can be tested in various ways; the transmitter outputs and receiver inputs are characterized for protection to the following limits: ± 15 kV using the Human Body Model, ± 8 kV using the Contact Discharge method specified in IEC61000-4-2, ± 15 kV using the Air-Gap Discharge method specified in IEC61000-4-2. The logic pins are characterized for protection to the following limit: ± 2 kV using the Human Body Model.

8 Parameter Measurement Information

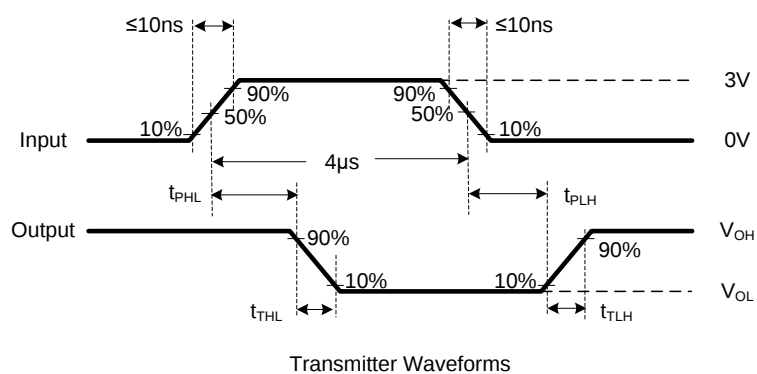
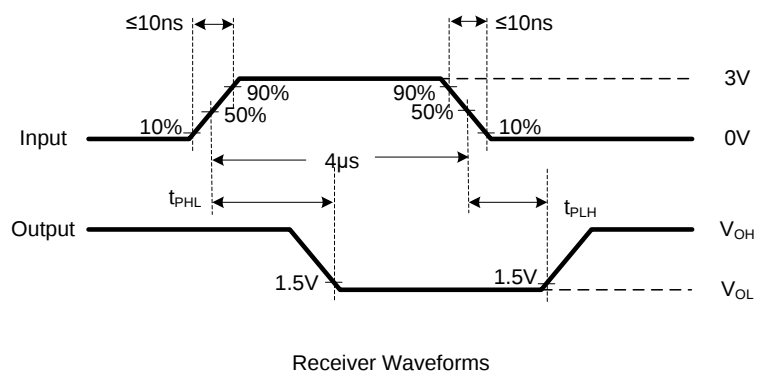
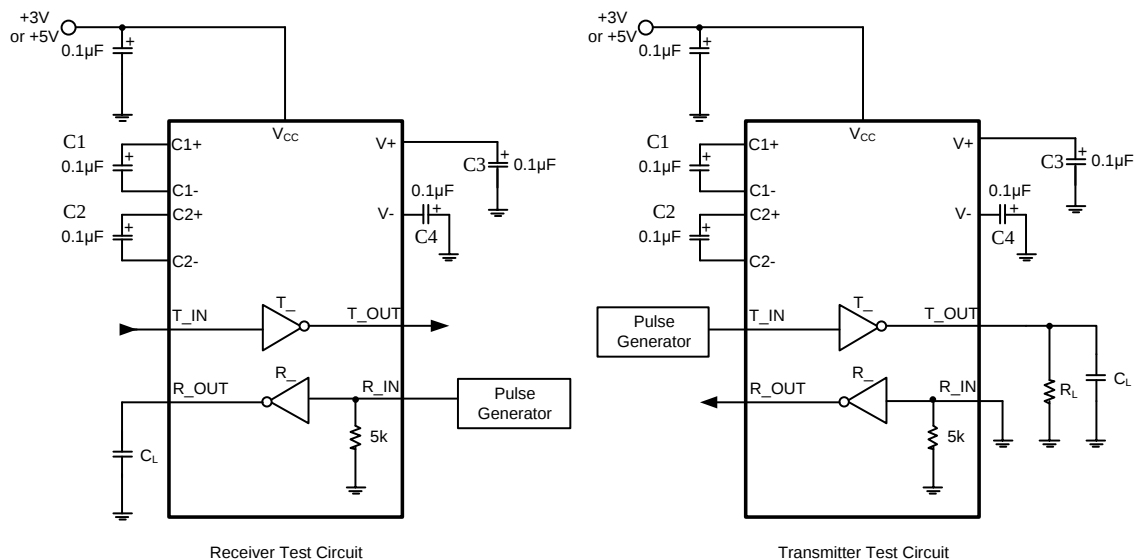


Figure 8-1

9 Typical Operating Circuits

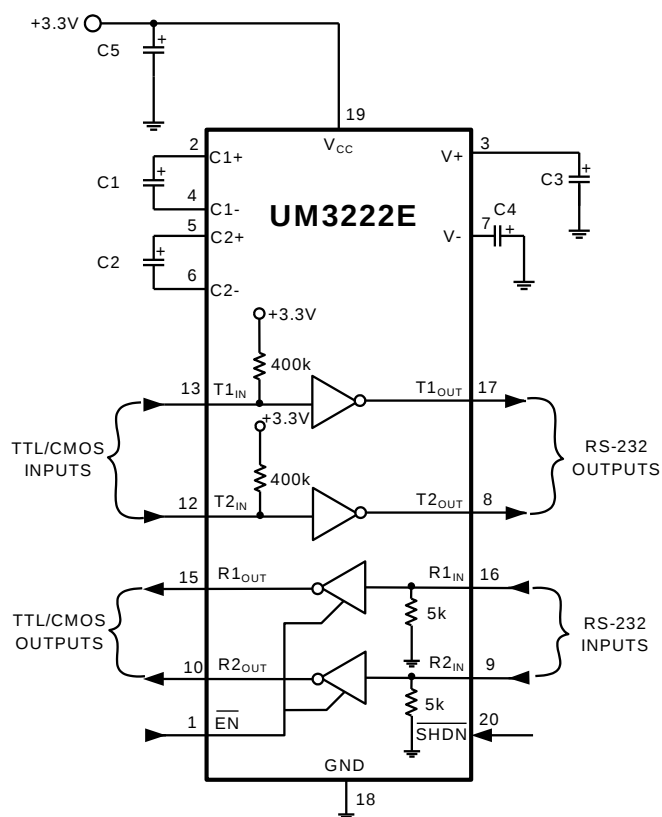
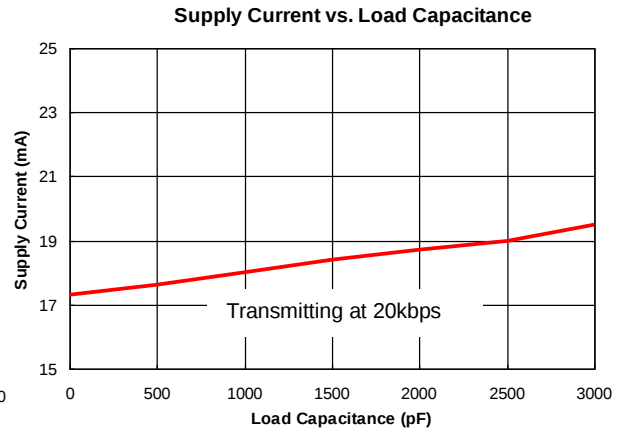
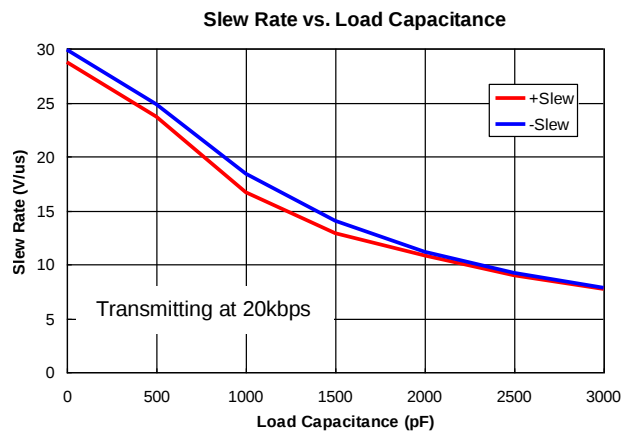
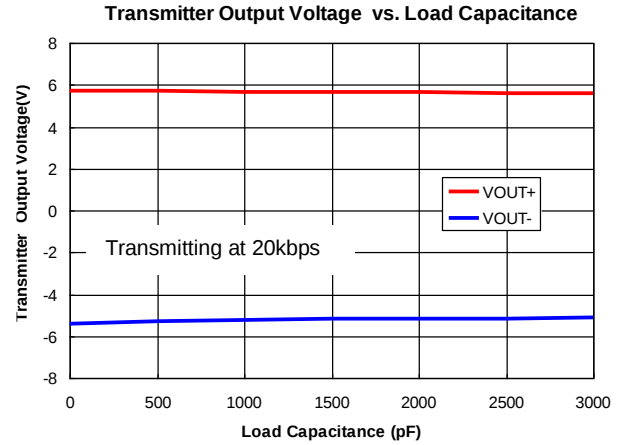
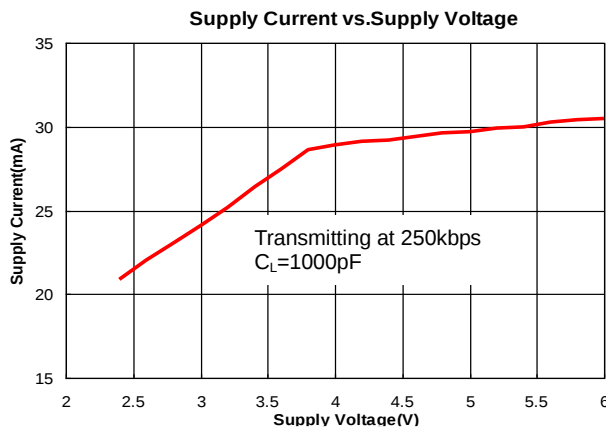
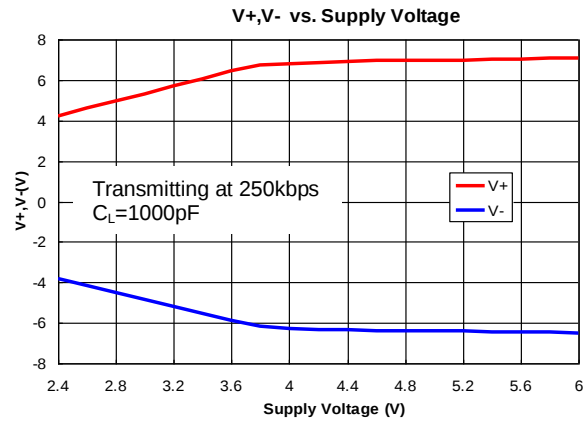
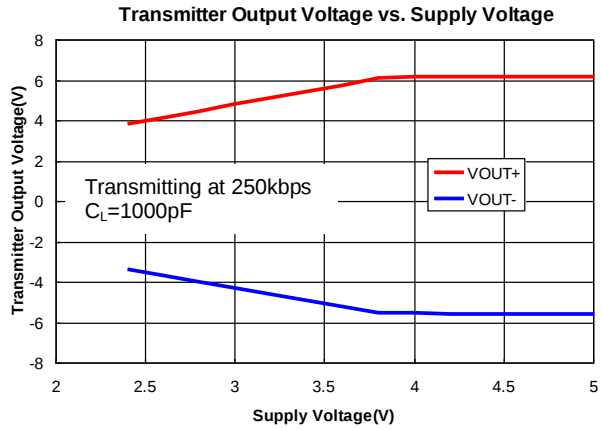


Figure 9-2

10 Typical Operating Characteristics

$V_{CC}=+3.3V$, 250kbps data rate, $0.1\mu F$ capacitors, all transmitters loaded with $3k\Omega$ and C_L , $T_A=25^\circ C$, unless otherwise noted.



11 Applications Information

11.1 Capacitor Selection

The capacitor type used for C1–C4 is not critical for proper operation; polarized or non-polarized capacitors can be used. The charge pump requires 0.1 μ F capacitors for 3.3V operation. Increasing the capacitor values (e.g. by a factor of 2) reduces ripples on the transmitter outputs and slightly reduces power consumption. C2, C3 and C4 can be increased without changing C1's value. When using the minimum required capacitor values, make sure the capacitor value does not degrade excessively with temperature. If in doubt, use capacitors with a larger nominal value. The capacitor's equivalent series resistance (ESR), which usually rises at low temperatures, influences the amount of ripples on V+ and V- output voltages.

The following table shows some recommended minimum required pump capacitor values for different input voltage ranges.

Minimum Required Pump Capacitor Value	
Input Voltage V_{CC}	Charge Pump Capacitor Value for UM3222E
2.7V to 3.6V	C1–C4=0.1 μ F
3.6V to 5.5V	C1–C4=0.47 μ F
2.7V to 5.5V	C1–C4=0.47 μ F

11.2 Power Supply Decoupling

In most circumstances, a 0.1 μ F V_{CC} bypass capacitor is adequate. In applications sensitive to power-supply noise, use a capacitor of the same value as charge pump capacitor C1. Connect bypass capacitors to the IC as close as possible.

11.3 Operation down to 2.7V

Transmitter outputs meet EIA/TIA-562 levels of ± 3.7 V with supply voltages as low as 2.7V.

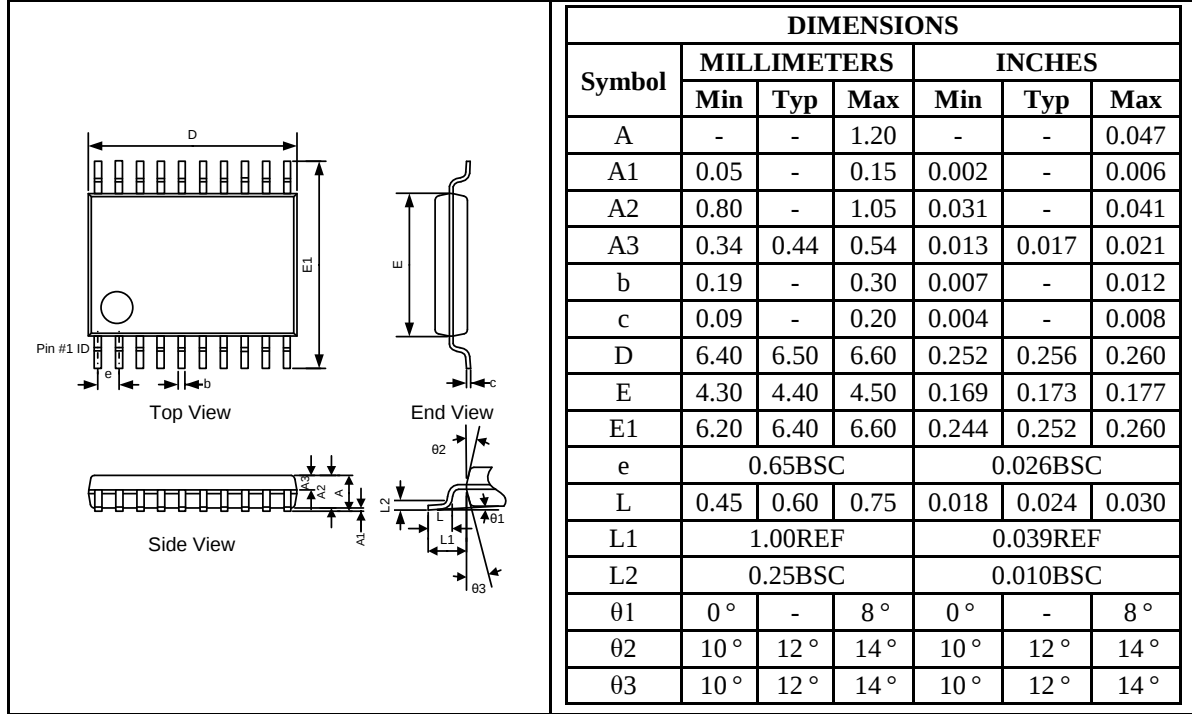
11.4 Interconnection with 3V and 5V Logic

The UM3222E can directly interface with various 3V and 5V logic families, including ACT and HCT CMOS.

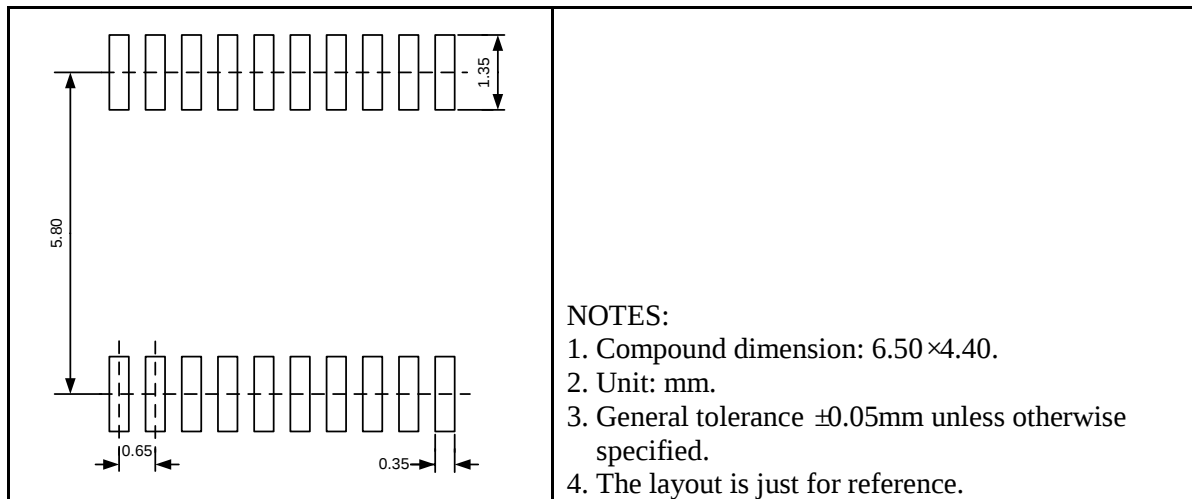
Package Information

TSSOP20

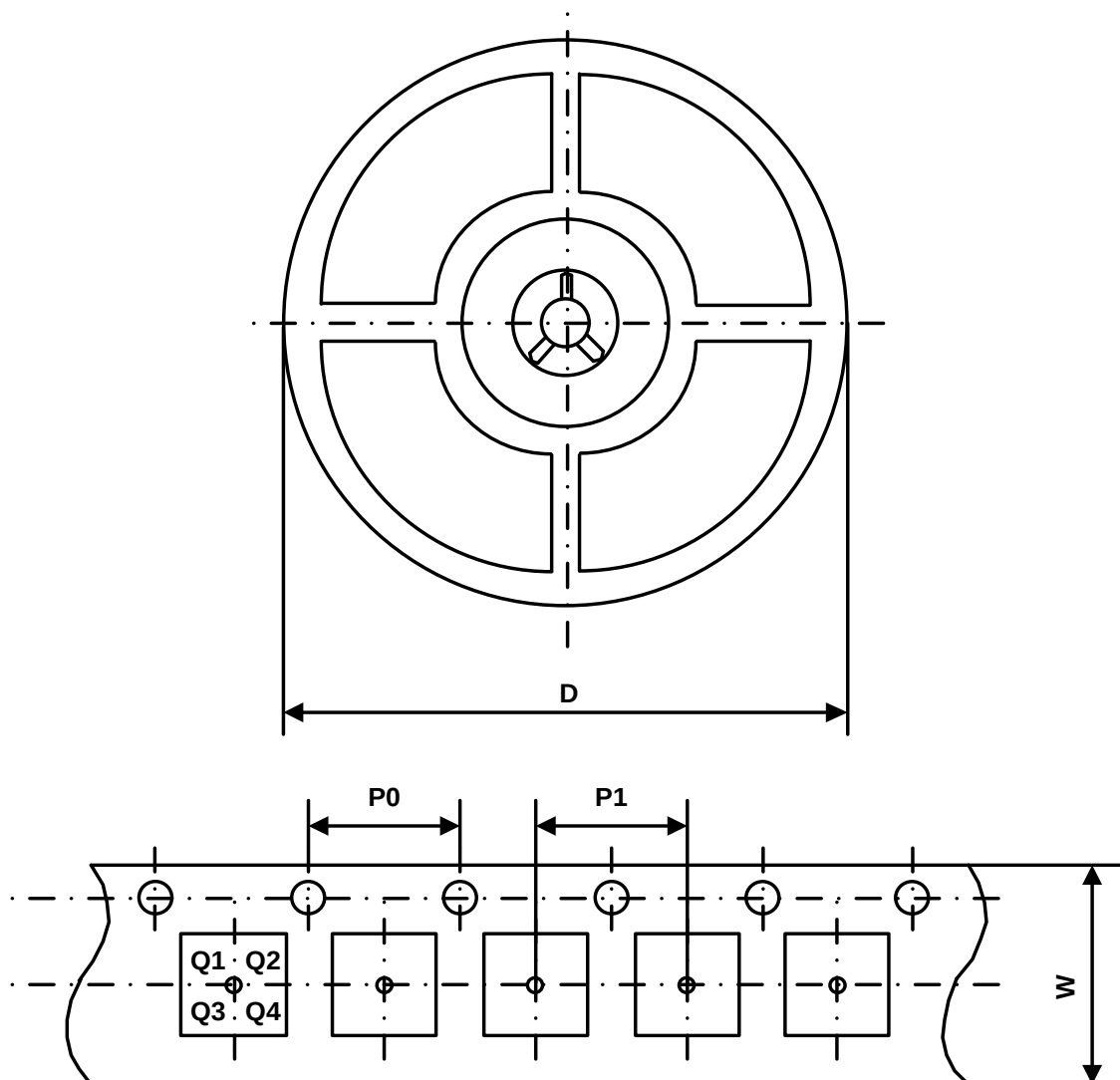
Outline Drawing



Land Pattern



Packing Information



Part Number	Package Type	Carrier Width (W)	Pitch (P0)	Pitch (P1)	Reel Size (D)	PIN 1 Quadrant
UM3222EEUE	TSSOP20	12 mm	4 mm	8 mm	330 mm	Q1

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